Final Exam

Final Exam – 25 points

Wednesday, May 8, 1:30pm-4:15pm

Review Sessions

Monday, May 6, 1:30pm-2:45pm – Joseph

Tuesday, May 7, 1:30pm-2:45pm – Viet

Part II – Typical Problems

1. (pseudocode, interface) => block diagram
2. (pseudocode, interface, block diagram) => ASM chart
3. actions => control signals in ASM charts
4. Memory map & textual specification => wrapper that modifies a circuit into an MMIO unit of an FPro system (see Quiz 2 & 3)

Part I – Typical Problems

1. fill in the blanks
2. circuits with a regular structure (for-generate)
3. simple block diagrams
4. VHDL code => block diagram
5. timing waveforms based on an ASM chart & inputs
6. VGA display system
7. FPro Video Subsystem (tile RAM, font RAM, frame buffer core, sprite core, On-Screen Display core, etc.)
8. FPro MMIO Subsystem (timer core, GPI core, GPO core, etc.)

Exams from Previous Years

Spring 2008-Spring 2018