

ECE 448

Midterm Exam

March 2, 2015

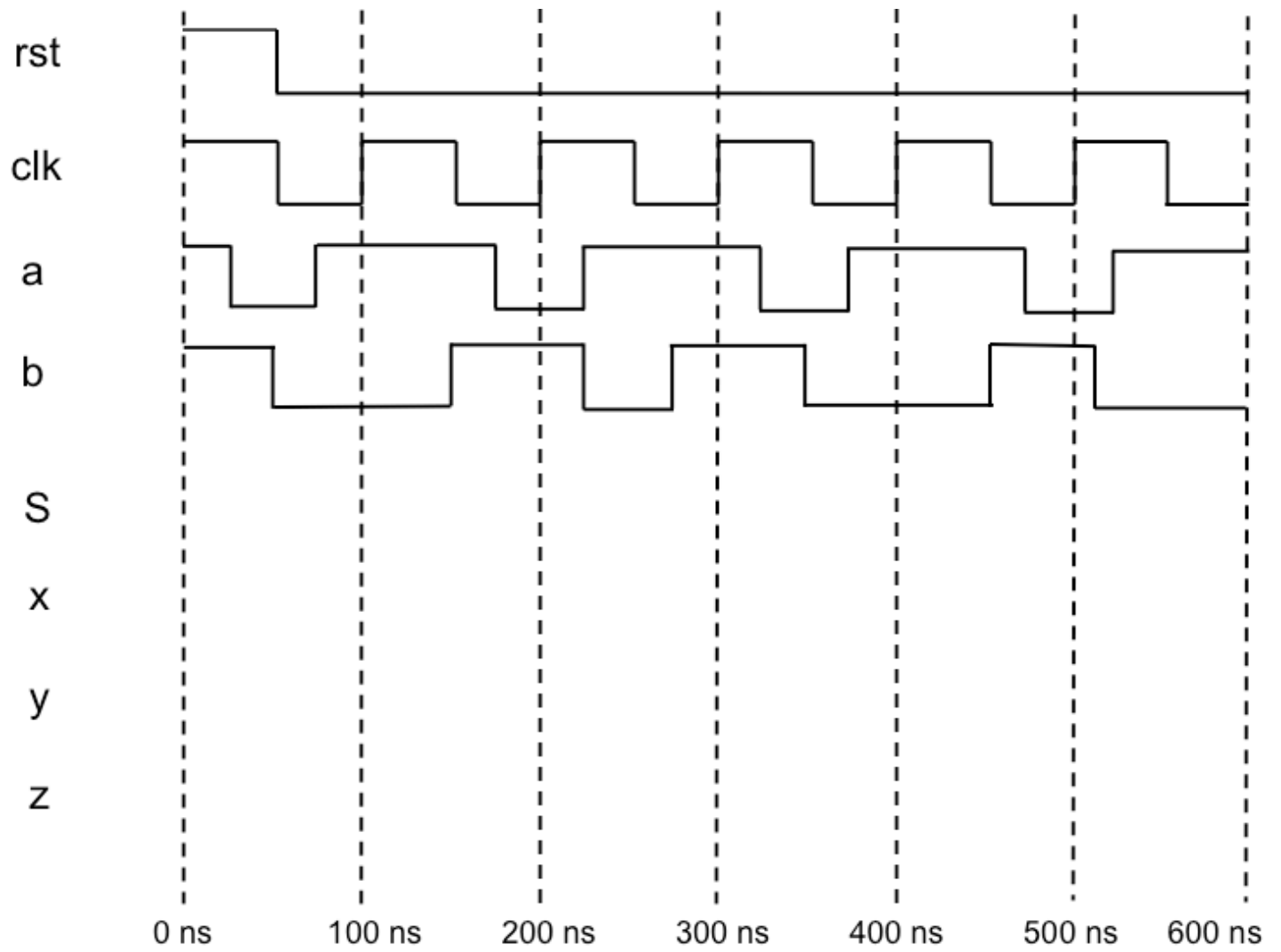
HONOR CODE PLEDGE: “On my honor I have neither given nor received aid on this exam”

Your first and last name (printed):

Your signature

Failure to sign the pledge may result in receiving no credit for the exam.

Problem 1A – Timing waveforms based on the ASM chart



Problem 1B – Dataflow VHDL code for the output function only

Problem 2 – Block Diagrams and Resource Utilization

A.

B.

Problem 2 – Block Diagrams and Resource Utilization (cont.)

C.

D.

Problem 3 – VHDL Code of the Variable Arithmetic Shifter Right

Problem 4 – Simple Testbench