

ECE 448

Midterm Exam

February 29, 2016

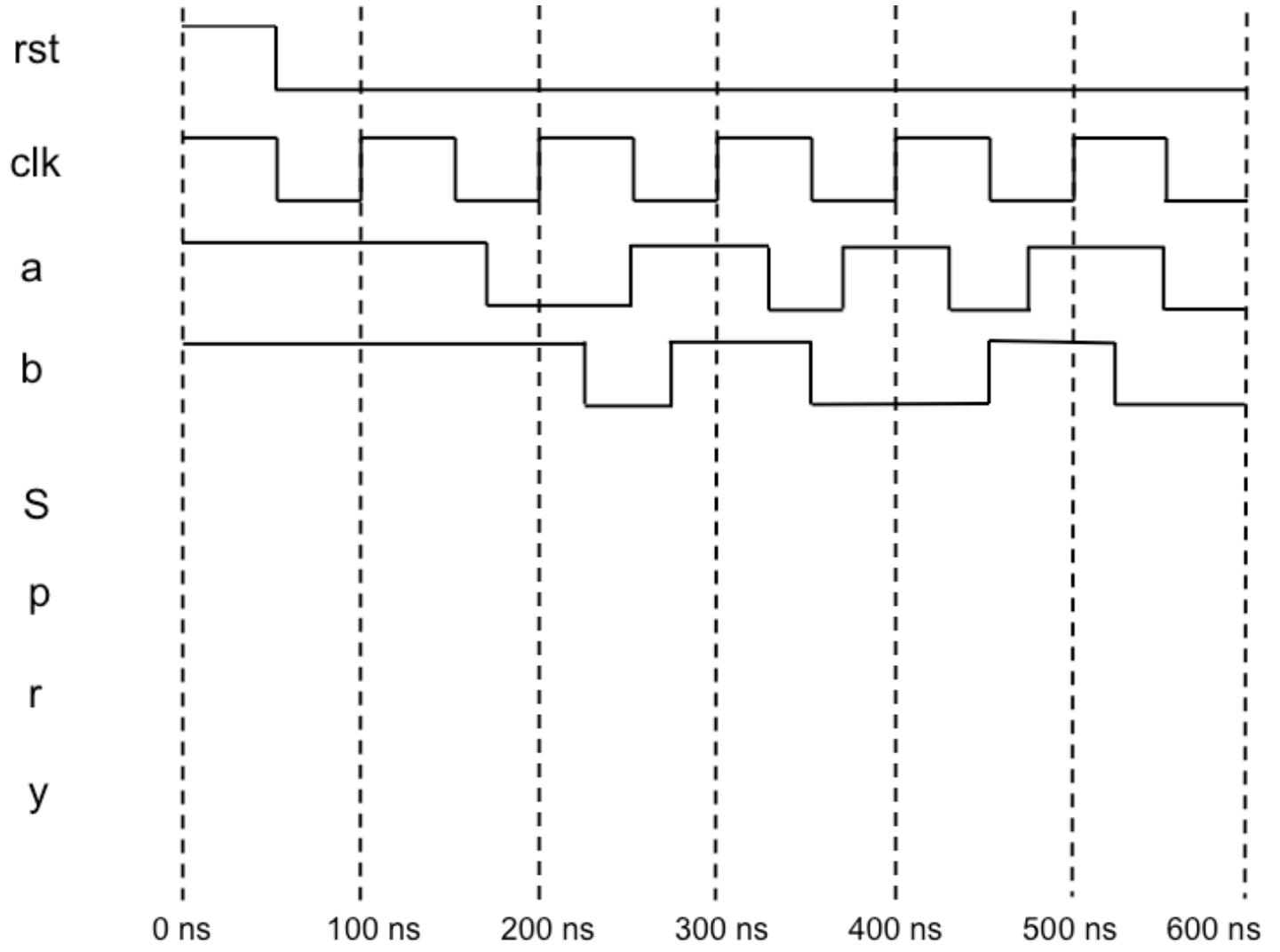
HONOR CODE PLEDGE: “On my honor I have neither given nor received aid on this exam”

Your first and last name (printed):

Your signature

Failure to sign the pledge may result in receiving no credit for the exam.

Problem 1 – Timing waveforms based on the ASM chart



Problem 1 – VHDL code

Problem 2 – Block diagrams and resource utilization

A.

B.

Problem 2 – Block diagrams and resource utilization (cont.)

C.

D.

Problem 3 – Block diagram

Problem 3 – VHDL code

Problem 4 – Simple Testbench

Problem 4 – Simple Testbench (cont.)