

Resource Utilization

| Resource Type | Number |
|-------------------|-----------|
| CLB slices | 25 |
| LUTs | 74 |
| Flip-flops | 37 |
| IO pins | 29 |

Corresponding fragments of the Vivado reports:

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+-----+-----+-----+-----+
|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Slice                        | 25 | 0     | 8150     | 0.31  |
|   SLICEL                      | 20   | 0     |          |       |
|   SLICEM                      | 5    | 0     |          |       |

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|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Slice LUTs                   | 74 | 0     | 20800    | 0.36  |
|   LUT as Logic                | 58   | 0     | 20800    | 0.28  |
|   LUT as Memory               | 16   | 0     | 9600     | 0.17  |
|     LUT as Distributed RAM    | 16   | 0     |          |       |
|     LUT as Shift Register     | 0    | 0     |          |       |
|
| Slice Registers             | 37 | 0     | 41600    | 0.09  |
|   Register as Flip Flop      | 37   | 0     | 41600    | 0.09  |
|   Register as Latch          | 0    | 0     | 41600    | 0.00  |

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|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Bonded IOB                   | 29 | 0     | 106     | 27.36 |
|   IOB Master Pads            | 14   |       |          |       |
|   IOB Slave Pads             | 14   |       |          |       |

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