

## Resource Utilization

Resource Type	Number
<b>CLB slices</b>	<b>37</b>
<b>LUTs</b>	<b>80</b>
<b>Flip-flops</b>	<b>68</b>
<b>IO pins</b>	<b>41</b>

Corresponding fragments of the Vivado reports:

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+-----+-----+-----+-----+
|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Slice                               | 37 | 0 | 8150 | 0.45 |
|   SLICEL                               | 29 | 0 | | |
|   SLICEM                               | 8 | 0 | | |

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|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Slice LUTs                               | 80 | 0 | 20800 | 0.38 |
|   LUT as Logic                               | 63 | 0 | 20800 | 0.30 |
|   LUT as Memory                               | 17 | 0 | 9600 | 0.18 |
|     LUT as Distributed RAM                     | 17 | 0 | | |
|     LUT as Shift Register                       | 0 | 0 | | |
|
| Slice Registers                           | 68 | 0 | 41600 | 0.16 |
|   Register as Flip Flop                       | 68 | 0 | 41600 | 0.16 |
|   Register as Latch                           | 0 | 0 | 41600 | 0.00 |

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|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Bonded IOB                               | 41 | 0 | 106 | 38.68 |
|   IOB Master Pads                             | 20 | | | |
|   IOB Slave Pads                             | 20 | | | |

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