

Resource Utilization

Resource Type	Number
CLB slices	22
LUTs	60
Flip-flops	55
IO pins	14

Corresponding fragments of the Vivado reports:

```

+-----+-----+-----+-----+
|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Slice                        | 22 | 0     | 8150      | 0.27  |
|   SLICEL                      | 17   | 0     |           |       |
|   SLICEM                      | 5    | 0     |           |       |

```

```

+-----+-----+-----+-----+
|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Slice LUTs                   | 60 | 0     | 20800     | 0.29  |
|   LUT as Logic                | 60   | 0     | 20800     | 0.29  |
|   LUT as Memory               | 0    | 0     | 9600      | 0.00  |
|
| Slice Registers             | 55 | 0     | 41600     | 0.13  |
|   Register as Flip Flop      | 55   | 0     | 41600     | 0.13  |
|   Register as Latch          | 0    | 0     | 41600     | 0.00  |

```

```

+-----+-----+-----+-----+
|                               | Used | Fixed | Available | Util% |
+-----+-----+-----+-----+
| Bonded IOB                   | 14 | 0     | 106       | 13.21 |
|   IOB Master Pads            | 7    |       |           |       |
|   IOB Slave Pads             | 6    |       |           |       |

```