Spring 2007

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday	Labs/Reading
January 22	23 Lecture 1 Introduction	24	25 Lecture 2 Jump Start	26	**27**	**28**	Intro 1, 2.1-2.4, 2.9-2.10
29	30 Lecture 3 Boolean Alg. HW1 Due	31	**February 1** Lecture 4 K-Maps	2	**3**	**4**	Equipment Intro 2.5-2.8 4.1-4.5
5	6 Lecture 5 K-Map/Design HW2 Due	7	8 Lecture 6 Number Systems	9	**10**	**11**	ActiveHDL Intro 5.1
12	13 Lecture 7 Comp Arith HW3 Due	14	15 Lecture 8 Codes and Design	16	**17**	**18**	Experiment 1 5.2-5.3, 5.8
19	20 Lecture 9 Adder in VHDL HW4 Due	21	22 Lecture 10 Multi-bit Adder	23	24	**25**	New Experiment 2 5.4-5.7
26	27 Lecture 11 Mux/Decoder HW5 Due	28	**March 1** Lecture 12 Encoder	2	**3**	**4**	Experiment 4 6.1-6.7
5	6 Lecture 13 Review HW6 Due	7	8 **Midterm**	9	**10**	**11**	Experiment 8 Exam Prep
12	13	14	15	16	**17**	**18**	Spring Break
19	20 Lecture 14	21	22 Lecture 15	23	**24**	**25**	ECE332 Midterm Chapter 3
26	27 Lecture 16 HW7 Due	28	29 Lecture 17	30	31	**April 1**	
2	3 Lecture 18 HW8 Due	4	5 Lecture 19	6	**7**	**8**	Chapter 7
9	10 Lecture 20 HW9 Due	11	12 Lecture 21	13	**14**	**15**	

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16	17 Lecture 22 HW10 Due	18	19 Lecture 23	20	**21**	**22**	Chapter 8
23	24 Lecture 24 HW11 Due	25	26 Lecture 25	27	28	**29**	Chapter 9
30	**May 1** Lecture 26 HW12 Due	2	3 Lecture 27 Review	4	**5**	**6**	
7	8	9	10 **Final Exam**	11	**12**	**13**	
14	15	16	17	18	**19**	**20**	

The Course Schedule is Subject to Change!!!