

## Fall 2011

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday	Reading/Labs
August 29 Introduction	30	31 SW Develop. C/Assembly	September 1	2	3	4	3, 4.1-4.2 Intro & digital/C Review
5 Labor Day	6	7 MSP430 Introduction	8	9	10	11	1-2 IAR IDE & Lab 0 (C & ASM)
12 MSP430 Arch Instructions	13	14 SW Develop C/Assembly	15	16	17	18	5.1-5.6; 4.3-4.8 ADC & Polling & LCD (w/ Demo)
19 I/O Interfacing	20	21 Polling & Interrupts	22	23	24	25	7.1-7.5; 5.7,6.5-6.7 IO & Prototyping <b>Lab 1 Due</b>
26 Reset & Interrupts	27	28 Interrupts & Low-Power	29	30	October 1	2	6.8-6.10 Assembly & Interrupts
3 Timer Input Capture	4	5 Timer Output Compare	6	7	8	9	5.8, 8.1-8.10 Timer <b>Lab 2 Due</b>
10 Columbus Day Recess	11 External LCD	12 RTC	13	14	15	16	7.6-7.9, 8.11
17 Keypad SW Scanning	18	19 Keypad HW Scanning	20	21	22	23	7.2-7.3 External LCD & Keypad <b>Lab 3 Due</b>
24 Review	25	26 <b>Midterm Exam</b>	27	28	29	30	Midterm Review
31 SPI	November 1	2 I2C	3	4	5	6	10.1-10.11 <b>Lab Midterm Exam</b>
7 RS232	8	9 Analog Comparator	10	11	12	13	10.12-10.14; 9.1 Serial <b>Lab 4A Due</b>
14 ADC	15	16 ADC & DAC	17	18	19	20	9.2-9.12 <b>Lab 4B Due</b>

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday	Labs/Reading
November 21 Advanced Applications	22	23	24	25	26	27	SLAA378B
Thanksgiving Recess							
28 Optimization	29	30 MSP430 Variants	<b>December 1</b>	2	3	4	SLAU132C;11.1-2 ADC & DAC  <b>Lab 5A Due</b>
5 Senior Design Use of MSP	6	7 Review	8	9	10	11	11.3  <b>Lab 6A Due</b>
12 Reading Day	13	14	15	16	17	18	  <b>Lab 5B Due</b>
19 <b>Final Exam</b> (4:30-7:15pm)	20	21	22	23	24	25	

The Course Schedule is Subject to Change!!!