

Spring 2016

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday	Reading/Labs
January 18 Martin Luther King Day	19	20 Introduction SW Develop.	21	22	23	24	D:3, 4.1-4.5, J3.1-3.6, 4.1-4.3 Intro & digital/C Review
25	26	27 C/Assembly MSP 430 Introduction	28	29	30	31	D:1-2, J1.1-1.4 CCS IDE & Lab 0 (C & ASM)
February 1 MSP430 Arch Instructions HW 1 Due	2	3 I/O Interfacing SW Develop. C/Assembly	4	5	6	7	D:4.6-4.8; 7.1-7.6 J:3.7-3.8, 8.1-8.5 C&ASM, Interfacing Lab 1 Due
8 Polling & Interrupts HW 2 Due	9	10 Reset & Interrupts	11	12	13	14	D:5.1-5.7,6.5-6.7; 1) IO & Polling Prototyping Lab 2 Due
15 HW 3 Due	16	17 Interrupts & Low-Power	18	19	20	21	D:6.8-6.10, SLAU367: 1.3-1.6 IO & Interrupts Lab 3 Due
22 Timer Output Compare HW 4 Due	23	24 Keypad SW Scanning	25	26	27	28	D:5.8, 8.3-8.9 J: 7.4 Timer, PWM Debounce
29 Keypad HW Scanning HW 5 Due	March 1	2 Timer Input Capture	3	4	5	6	D:7.2-7.3; J: 8.3 Keypad Lab 4 Due
7	8	9	10	11	12	13	Spring Break
14 WDT and RTC HW 6 Due	15	16 Review	17	18	19	20	D:8.1-8.2, 8.10-8.11 Midterm Review Lab 5 Due
21 Midterm Exam	22	23 SPI	24	25	26	27	ADC Lab 6 Due
28 SPI HW 7 Due	29	30 External LCD	31	April 1	2	3	D:10.1-10.11 Lab Midterm Exam
4 I2C HW 8 Due	5	6 RS 232	7	8	9	10	D:10.12-10.14; 9.1 External LCD via SPI

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11 Comparator ADC HW 9 Due	12	13 ADC / DAC	14	15	16	17	D:9.2-9.12 I2C Sensor Lab 7 Due
18 USB HW 10 Due	19	20 USB	21	22	23	24	SLAU132C;11.1-2 SLAA378B Lab 8 Due
25 DMA HW 11 Due	26	27 Power and Clock Sources	28	29	30	May 1	D:11.3 Lab 9a Due
2 Review HW 12 Due	3	4 Review	5 Exam Period Lab 4:30 pm - 6:30 pm	6 Lab 4:30 pm - 6:30 pm	7	8	Lab 9b Due
9 Final Exam (9:45 am - 11:45 am)	10	11	12	13	14	15	

J:1.1-1.3 Reading from Jiménez et al. Chapters 1.1 through 1.3

D:3, 4.1-4.2 Reading from Davies Chapters 3 and 4.1 through 4.2

Cancellation due to Weather	Morning	Whole Day
	Evening	

The Course Schedule is Subject to Change!!!

Additional Reading Assignments

- 1) TI SLAU132 6.1-6.8, J 3.9-3.10, 4.4, 4.7-4.8, 5.4, 7.1-7.3